

ENLISHE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:

Arkadiy Morgenshtein et al.

Serial No.:

10/648,474

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For:

LOGIC CIRCUIT AND METHOD FOR

LOGIC CIRCUIT DESIGN

Examiner:

Daniel D. Chang

Group Art Unit: 2819

Attorney

Docket: 26327

RESPONSE

Commissioner for Patents PO Box 1450 Alexandria, VA 22313-1450

Sir:

This is in response to the United States Patent and Trademark Office Action mailed February 7, 2005, which response is being made on or before May 7, 2005.